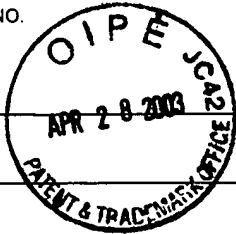


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| FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY) | ATTY. DOCKET NO. ARC.003A |  | APPLICATION NO. 09/801,241 |
| | APPLICANT David Latta | | |
| | FILING DATE March 7, 2001 | GROUP 2181 | |

| U.S. PATENT DOCUMENTS | | | | | | | |
|-----------------------|--|-----------------|------|------|-------|----------|---------------------------------|
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE (IF APPROPRIATE) |
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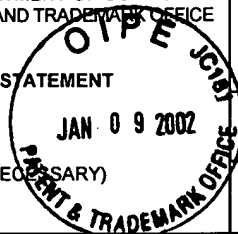
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|--------------------------|---|-----------------|--------|---------|-------|----------|-------------|----|
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
| | | | | | | | YES | NO |
| : <i>AKM</i> | 1 | EP 0 479 390 A2 | 4/8/92 | EP | | | | |
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| | | | | | | | | |

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| AKM | 2 | The CoreConnect Bus Architecture – XP-002204519 by International Business Machines Corporation, 1999. |
| | 3 | Embedded DRAM for a Reconfigurable Array – XP-000894790 by Symposium on VLSI Circuits Digest of Technical Papers |
| | 4 | A Scalable Architecture for System-On-Chip Interconnections – XP-002204520 by Pierre Guerrier and Alain Greiner dated October 22, 1999. |
| ↓ | 5 | A Reconfigurable Bus IP for Modular Function Integration – XP-002204520 by Neil S. Stollen, Ph.D., P.E. at Infinite Technology Corp. dated March 22, 1999. |
| EXAMINER | DONNA A. MASON DATE CONSIDERED 12-8-2003 | |

*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

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| FORM PTO-1449 | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | ATTY. DOCKET NO. ARC.003A | APPLICATION NO. 09/801,241 |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | APPLICANT David Latta | |
| (USE SEVERAL SHEETS IF NECESSARY) | | FILING DATE March 7, 2001 | GROUP 2181 |



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| | | | | | | | YES NO |
| | | | | | | | |

| EXAMINER INITIAL | OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.) | |
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| AKM | 17 | Application Serial No. 09/418,663 entitled "Method and Apparatus for Managing the Configuration and Functionality of a Semiconductor Design," filed October 14, 1999 – Attorney Docket No. ARC.001A |
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| EXAMINER <i>Barbara A. Mason</i> | DATE CONSIDERED <i>12-8-2003</i> |
| *EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT. | |